



Figure similar

\*\*\* Spare part \*\*\* SIMATIC DP, Electronics module for ET 200S, 2 AI Standard U 15 mm width, +/-10V; 13 bit+sign +/-5 V; 12 bit+sign, 1..5 V; 12-bit Cycle time 65 ms/channel with SF LED (group fault)

General information	
Product function	
<ul style="list-style-type: none"> <li>• Isochronous mode</li> </ul>	No
Supply voltage	
Load voltage L+	
<ul style="list-style-type: none"> <li>• Rated value (DC)</li> <li>• Reverse polarity protection</li> </ul>	24 V; From power module Yes
Input current	
from load voltage L+ (without load), max.	30 mA
from backplane bus 3.3 V DC, max.	10 mA
Power loss	
Power loss, typ.	0.6 W
Address area	
Address space per module	
<ul style="list-style-type: none"> <li>• Address space per module, max.</li> </ul>	4 byte
Analog inputs	
Number of analog inputs	2
permissible input voltage for voltage input (destruction limit), max.	35 V; 35 V continuous; 75 V for max. 1 ms (mark to space ratio 1:20)
Cycle time (all channels) max.	Number of active channels per module x basic conversion time
Input ranges (rated values), voltages	
<ul style="list-style-type: none"> <li>• 1 V to 5 V</li> <li>• -10 V to +10 V</li> <li>• -5 V to +5 V</li> </ul>	Yes Yes Yes
Cable length	
<ul style="list-style-type: none"> <li>• shielded, max.</li> </ul>	200 m
Analog value generation for the inputs	
Measurement principle	integrating
Integration and conversion time/resolution per channel	
<ul style="list-style-type: none"> <li>• Resolution with overrange (bit including sign), max.</li> <li>• Integration time, parameterizable</li> <li>• Integration time (ms)</li> <li>• Interference voltage suppression for interference frequency f1 in Hz</li> <li>• Conversion time (per channel)</li> </ul>	14 bit; ±10 V: 13 bit + sign, ±5 V: 13 bit + sign; 1 to 5 V: 13 bit Yes 16,7 / 20 ms 50 / 60 Hz 65 ms; 55 / 65 ms
Smoothing of measured values	
<ul style="list-style-type: none"> <li>• parameterizable</li> <li>• Step: None</li> </ul>	Yes; In four stages by means of digital filtering Yes; 1x cycle time

- Step: low Yes; 4x cycle time
- Step: Medium Yes; 32x cycle time
- Step: High Yes; 64x cycle time

#### Errors/accuracies

Operational error limit in overall temperature range

- Voltage, relative to input range, (+/-) 0.6 %

Basic error limit (operational limit at 25 °C)

- Voltage, relative to input range, (+/-) 0.4 %

#### Interrupts/diagnostics/status information

Diagnoses

- Wire break Yes; Measuring range 1 to 5 V only
- Group error Yes
- Overflow/Underflow Yes

Diagnostics indication LED

- Group error SF (red) Yes

#### Parameter

Remark	4 byte
Diagnostics wire break	Disable / enable (only in measuring range 1 to 5 V)
Group diagnostics	Disable / enable
Overflow/Underflow	Disable / enable

#### Potential separation

Potential separation analog inputs

- between the channels No
- between the channels and backplane bus Yes
- Between the channels and load voltage L+ Yes

#### Isolation

Isolation tested with 500 V DC

#### Dimensions

Width	15 mm
Height	81 mm
Depth	52 mm

#### Weights

Weight, approx. 40 g

#### Classifications

	Version	Classification
eClass	14	27-24-26-01
eClass	12	27-24-26-01
eClass	9.1	27-24-26-01
eClass	9	27-24-26-01
eClass	8	27-24-26-01
eClass	7.1	27-24-26-01
eClass	6	27-24-26-01
ETIM	10	EC001596
ETIM	9	EC001596
ETIM	8	EC001596
ETIM	7	EC001596
IDEA	4	3562
UNSPSC	15	32-15-17-05

#### Approvals / Certificates

General Product Approval



[Miscellaneous](#)

[Manufacturer Declaration](#)



[Metrological Approval](#)



[China RoHS](#)



For use in hazardous locations



[EM](#)



For use in hazardous locations

Maritime application

[Miscellaneous](#)

[CCC-Ex](#)



Maritime application

[NK / Nippon Kaiji Kyokai](#)



[CCS \(China Classification Society\)](#)

last modified:

8/22/2025